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Resume of Richard Wesley Hamming, 1979

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RESUME OF RICHARD WESLEY HAMMING

Richard W. Hamming was born on 11 February 1915 in Chicago, Illinois. He received a B.S. degree from the University of Chicago in 1937, and M.S. from the University of Nebraska in 1939, and a Ph.D. from the University of Illinois in 1942.

From 1945 until 1946 he worked on computing atomic bomb designs on IBM equipment for the Manhattan Project at Los Alamos, New Mexico. In 1946 he joined Bell Telephone Laboratory in Murray Hill, New Jersey, where he ran analog as well as digital computer centers in the Math and Computing Research Department until July 1976.

During the period 1946 until 1976 he taught evenings at New York University, City College of New York, and Stevens Institute. He also spent two days a week as Adjunct Professor of Statistics at Princeton University from 1971-74.

He spent the year 1960-61 on sabbatical leave at Stanford University and the year 1970-71 on sabbatical at the University of California at Irvine.

In September 1976 he joined the faculty of the Naval Postgraduate School, Monterey, California where he is teaching in the Department of Computer Science. His research interest is in numerical methods, software, simulation and system performance and measurement.

He is a member of the National Academy of Engineering. He is past President of the Association for Computing Machinery and received their Turing prize. He is a Fellow of the Institute of Electrical and Electronic Engineers (IEEE). He presented the paper "Error Detecting and Error Correcting Codes (BSTJ)" and received a patent on it. He invented the General Purpose System for IBM 650 which is widely used. He was a member of the Board of Directors of Scientific Data Systems when sold to Xerox, and is the editor of numerous journals. Professor Hamming received the Piore Award from IEEE in 1979.

He is the author of seven books and over sixty-five papers.

